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**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>lv</i>	A. 6,463,522	10/08/2002	AKKARY	712	216	
<i>lv</i>	B. 6,016,542	12/18/2000	GOTTLIEB ET AL.	712	225	
C.						
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E.						
F.						
G.						
H.						
I.						

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
J.							
K.							
L.							
M.							
N.							

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

<i>lv</i>	O.	Marr, Deborah T., et al. "Hyper-Threading Technology Architecture and Microarchitecture" <a href="http://developer.intel.com/sites/corporate/tradmarx.htm">http://developer.intel.com/sites/corporate/tradmarx.htm</a> . Intel Corporation, February 2002.
<i>lv</i>	P.	"Introduction to Hyper-Threading Technology" <a href="http://www.intel.com/technology/hyperthread/download/25000802.pdf">http://www.intel.com/technology/hyperthread/download/25000802.pdf</a> . Intel Corporation, 2001.
<i>lv</i>	Q.	"Intel® XScale™ Microarchitecture for the PXA255 Processor-User's Manual" Table of Contents, and Appendix A. <a href="http://www.intel.com/design/pca/applicationsprocessors/manuals/27879601.pdf">http://www.intel.com/design/pca/applicationsprocessors/manuals/27879601.pdf</a> . Intel Corporation, March 2003.
	R.	
	S.	
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**EXAMINER**
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